

determining a load capacitance for each of said time instances, said load capacitance specifying a capacitance from said driving point of said circuit;

determining operation of said circuit at a new time instance based on said load capacitance of a previous time instance; and

determining timing delay parameters based on a response to said RC network at said time instances.

22. (Once Amended) A computer readable medium, comprising a plurality of instructions, which when executed by a computer, causes the computer to determine timing delays for a circuit in an integrated circuit, said instructions for:

determining a resistive-capacitive ("RC") network between a driving point and a receiving point, said circuit driving said RC network at said driving point;

selecting a plurality of time instances for analysis of said circuit;

determining a load capacitance for each of said time instances, said load capacitance specifying a capacitance from said driving point of said circuit;

determining operation of said circuit at a new time instance based on said load capacitance of a previous time instance; and

determining timing delay parameters based on a response to said RC network at said time instances.